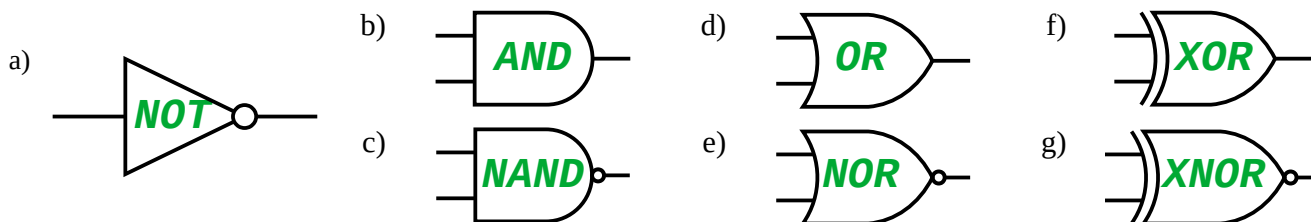
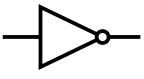
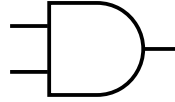
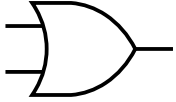
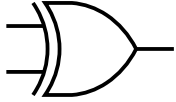


**Boolean Algebra – Worksheet 1**

1. Write the name of each logic gate inside the gate (AND, NAND, NOR, NOT, OR, XNOR, XOR)



2. Fill in the table.

				
gate name	<b>NOT</b>	<b>AND</b>	<b>OR</b>	<b>XOR</b>
set notation	'	$\cap$	$\cup$	
logic symbol	$\neg$	$\wedge$	$\vee$	$\underline{\vee}$
Java bitwise operator	<code>~</code>	<code>&amp;</code>	<code> </code>	<code>^</code>

2. Fill in the truth table.

		AND	NAND	OR	NOR	XOR	XNOR
<b>0</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>0</b>	<b>1</b>	<b>0</b>	<b>1</b>
<b>0</b>	<b>1</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>1</b>	<b>0</b>
<b>1</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>1</b>	<b>0</b>
<b>1</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>1</b>	<b>0</b>	<b>0</b>	<b>1</b>

3. Complete the following truth tables.

a)

A	B	$\neg A$	$\neg B$	$\neg A \wedge \neg B$	$\neg(\neg A \wedge \neg B)$	$A \vee B$
<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>0</b>
<b>0</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>
<b>1</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>0</b>	<b>1</b>	<b>1</b>
<b>1</b>	<b>1</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>

b)

A	B	$\neg A$	$\neg B$	$\neg A \vee \neg B$	$\neg(\neg A \vee \neg B)$	$A \wedge B$
<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>0</b>
<b>0</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>1</b>	<b>0</b>	<b>0</b>
<b>1</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>0</b>
<b>1</b>	<b>1</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>

e) Note that the final two columns of each table show **De Morgan's Laws**. Write these two equivalencies below:

$$\neg(\neg A \wedge \neg B) = A \vee B$$

$$\neg(\neg A \vee \neg B) = A \wedge B$$